Communications & PR Review

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IESA®
INDIA ELECTRONICS & SEMICONDUCTOR ASSOCIATION
India's ESDM Leadership and Knowledge Partner

COVERAGE HIGHLIGHTS

Print - National

CHASING A FAB FINISH

Semicon mission sets ground for AI jump

TEAM TOI

The FM announced the launch of India Semiconductor Mission 2.0, giving impetus to building domestic capabilities in the semiconductor equipment, chemicals and gases market, estimated to be around $400 billion by 2030, according to India Electronics and Semiconductor Association (IESA) and SEMI India, an industry body.

This comes at a time of global supply bottlenecks due to increasing AI workloads. As capex and specific equipment are shifted towards high bandwidth memory and advanced DRAM for training and inference of AI, straining global original equipment makers (OEMs), experts say this presents a rare opportunity for India to build an ecosystem, both by encouraging global players to source more from Indian MSMEs and creating domestic champions in the longer-term.

Raja Manickam, founder and chief executive of fabless startup iVP Semi and industry veteran, said in the initial stages, incentives should encourage global OEMS source more from India. "The demand for new wafer fabs is extremely high because of AI," he said.

It could be the right time for India as the demand and lead times for chip equipment are longer, but only if AI demands hold up, says Sanjay Kumar, VP at AT Kearney and former senior director, US department of commerce, CHIPS Program office. "Indian internal consumption is not going to match major hubs like Taiwan and Korea, so production should target export markets, which only global OEMs are able to qualify for," he said.

In 5-7 years, India can build competitive strength in low to mid complexity segments such as sub-fab equipment vacuum systems, automation systems for wafer handling, precision mechanics and advanced packaging equipment, says Ashok Chandak, president of IESA and SEMI India.

THE TIMES OF INDIA

ISM 2.0 nudges EMS majors to climb the chip value chain

New mission builds on the ₹76,000-cr chip scheme to deepen the components ecosystem

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NEW DELHI

India's top listed electronics manufacturers, Syrma SGS and Dixon Technologies, are reassessing plans to enter semiconductor and chip component manufacturing after Budget 2026 unveiled India Semiconductor Mission (ISM) 2.0, a new incentive programme aimed at building deeper domestic chip capabilities.

The second mission, expected to follow the ₹76,000 crore chip incentive scheme launched five years ago, will focus on developing a more comprehensive semiconductor components ecosystem. The policy shift is prompting electronics manufacturing services (EMS) companies, mostly dependent on low-margin assembly work, to examine whether they can move up the electronics supply chain.

"Companies which missed out on ISM 1.0 have now got another window to foray into semiconductor manufacturing. We at Syrma SGS would be studying the policy guidelines and would seriously endeavour to enter this space," Jasbir Singh Gujral, managing director of Syrma SGS, told *Mint*.

Dixon Technologies took a more reserved stance. "We will evaluate (ISM 2.0), once the detailed guidelines are out," Saurabh Gupta, director finance and group chief financial officer at Dixon, told *Mint*.

Dixon has flagged semiconductor ambitions before. At its Q3FY25 earnings call a year ago, managing director Atul Lall told analysts it was "in active discussions" to set up a display fabrication plant with a foreign technology partner at a net investment of $3 billion. It has not materialized so far. Analysts say the appeal is clear even if near-term scale is limited.

"Most semiconductor projects will not come with the kind of scale that mobile phone and consumer electronics assemblies bring for EMS firms. But, venturing into semiconductors will prove significantly beneficial in long run as firms will be able to expand margins through vertical integration of a product line, and closer control of the entire electronics value chain," said Harshit Kapadia, vice-president at brokerage Elara Capital.

If the plans fructify, Syrma and Dixon will join a small but growing set of firms straddling electronics assembly and semiconductor manufacturing.

Unlisted Tata Electronics that began as a contract manufacturer assembling Apple iPhones, announced a chip fab in Dholera, Gujarat in February 2024 that may operationalize by mid-2027. Taiwan's Foxconn entered semiconductor fabrication in partnership with HCL Group last May. Kaynes Technology announced outsourced semiconductor assembly and testing (Osat) facility in September 2023. All these were backed under ISM's first tranche of incentives, alongside nine other mostly assembly- and testing-focused ventures.

Ashok Chandak, president of industry body India Electronics and Semiconductor Association (Iesa), said a semiconductor push could lift the long-term value of EMS firms.

"Electronics manufacturers are naturally suited to venture into the semiconductor industry as they already have sectoral expertise in high-tech manufacturing. For most EMS firms, the ability to venture into semiconductor component manufacturing would mean memory and storage chip assemblies, as well as compound chips for industrial electronics. All of this would add increasing value to the business that electronics manufacturers are pursuing right now," Chandak said.

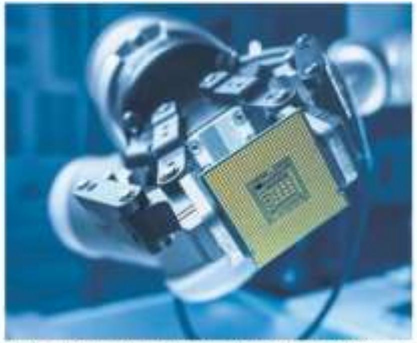
Capital remains a hurdle, but analysts do not see it as a major concern.

"Apart from Dixon, which has been battered in the markets because of its over-reliance on a slowing mobile phone market, all other EMS firms have grown through a tricky December. This shows that investors and shareholders are bullish on them, and it is unlikely that any of them will struggle to raise enough capital to venture into semiconductor fabrication and assembly," Elara's Kapadia added.

Pressures on existing business models are evident. India's largest listed EMS firm Dixon's revenue dipped 28% sequentially as the global mobile phone market slowed, while Syrma, with a more diversified mix spanning laptops, IT and telecom hardware and industrial electronics, posted 10% sequential revenue rise in Q3.

Ankush Wadhera, managing director and partner, and India leader for semiconductors at BCG, said ISM 2.0 is designed to push the ecosystem upstream.

The second chip incentives scheme will focus on "building upstream semiconductor capabilities beyond assembly-led growth—by focusing on equipment, materials, full-stack design, industry-led R&D, skilling and the creation of Indian IP," Wadhera said.



The policy push in Budget 2026 is prompting EMS firms to look beyond low-margin assembly and move up the electronics value chain ISTOCKPHOTO

MOVING UPSTREAM

SYRMA SGS has signalled its strong intent to enter the semiconductor manufacturing space

DIXON had earlier explored a $3-billion display fab and is now waiting for the detailed guidelines

TATA Electronics, Foxconn-HCL and Kaynes Tech already span assembly and semiconductors

CAPITAL needs are high, but analysts expect EMS firms to raise funds despite near-term pressures

mint

Business Standard

Higher ECMS outlay, ISM 2.0 send strong signal to electronics, chip industry

AASHISH ARYAN
New Delhi, 1 February

The Union Budget 2026-27's decision to increase the outlay for the less-than-a-year-old Electronics Component Manufacturing Scheme (ECMS), along with the announcement of the next phase of the India Semiconductor Mission (ISM), signals a renewed focus on enhancing India's position in the global electronics and semiconductor supply chain ecosystem, industry executives and experts said.

In her Budget speech on Sunday, Finance Minister Nirmala Sitharaman announced that the ECMS outlay, approved by the Union Cabinet in April 2025, will rise from ₹22,919 crore to ₹40,000 crore. She also announced that the government would soon launch the second phase of ISM, with ₹1,000 crore allocated for ISM 2.0 in the current financial year (2025-26).

The expansion of ECMS reaffirms the government's long-term commitment to building resilient domestic supply chains and strengthening India's role in global value chains, said the India Cellular & Electronics Association.

"The additional ₹40,000 crore outlay for ECMS could catalyse substantial investments, enhance domestic value addition, generate high-quality employment, and solidify India's emergence as a global leader in electronics manufacturing and innovation," said Sujay Shetty, managing director (MD) of electronics system design and manufacturing and semiconductor practice at PwC India.

Launched in April 2025, ECMS has so far approved 46 applications with a cumulative investment of ₹54,567 crore. These projects are estimated to generate ₹3.47 trillion in production and are expected to create direct employment for nearly 51,000 people.

The 46 projects, spread across 11 states, will produce electronics components including printed circuit boards, capacitors, connectors, mobile phone and other device enclosures, lithium-ion cells, camera and display modules, optical transceivers, aluminium extrusions, anode materials, and copper-clad laminates. These components feed into the manufacturing of products such as mobile phones, laptops, televisions, computer displays, and servers.

ILLUSTRATION: AJAYA KUMAR MOHANTY

"We had expected 50–55 applications for ECMS, but received 249 in total. The decision to raise the outlay to ₹40,000 crore will help sustain that momentum and bring more companies under the scheme," Union Minister for Electronics & Information Technology Ashwini Vaishnaw said during a post-Budget briefing on Sunday.

For ECMS to achieve its intended impact, it is crucial that the necessary infrastructure enablers are in place and the scheme is implemented efficiently. Timely execution will be key to translating policy intent into tangible outcomes, said Sanjeev Agarwal, executive director and chief manufacturing officer of homegrown electronics and mobile phone maker Lava International.

The announcement of ISM 2.0 itself is an important signal for India's semiconductor ambitions, marking a shift from a fabrication-centric approach to a full value-chain strategy covering equipment, materials, Indian intellectual property, and supply-chain resilience, said Ashok Chandak, president of the India Electronics and Semiconductor Association.

The move also reflects that India is no longer in catch-up mode within the semiconductor manufacturing ecosystem, said Shashwath TR, cofounder and chief executive officer (CEO) of Mindgrove Technologies. "By enabling industry-led research and development and fostering talent depth, ISM 2.0 unlocks scalable innovation, strengthens export competitiveness, and positions India to lead in high-growth semiconductor applications across artificial intelligence, the Internet of Things, and next-generation computing," he added.

Beyond these two, the exemption of basic Customs duty on select electrical appliances, including microwaves, is a timely initiative that will improve cost efficiencies, encourage domestic value creation, and boost market adoption, said Tadashi Chiba, MD and CEO of Panasonic India.

Bagmane Tech Park in Bengaluru

Photo: SHUTTERSTOCK

The Long Game

Karnataka's IT supremacy once again helps it secure pole position among states. But it is not resting easy on its laurels, but making a push for the future by focussing on hardware

• Tarunya Sanjay

When global innovation finds home in Bengaluru and Karnataka, it scales for the world, said Chief Minister Siddaramaiah late last year, setting the tone for what the next technological decade is going to look like for the state.

Karnataka is already a favourite among start-ups, given its position as the country's top information technology (IT) software hub. And, once again, it clinches the top spot in the *Outlook Business* Outperformers 2026 rankings. With its dominance in software exports and global IT services, the state secures an overall score of 83.3 and first place among peers for starting up.

Now, the state has made a clarion call to become an IT hardware hub too. Of course, this shift won't be easy. But Karnataka has all the ingredients—significant high-tech exports and a thriving services sector—to make it happen.

As India pushes into electronics manufacturing, electric vehicles (EVs) and semiconductors, the state is positioning itself as the nerve centre of design, engineering and systems integration. This is also a competitive response.

"This is a conscious choice. Karnataka is not trying to compete on who can offer the cheapest factory floor. Historically, manufacturing has followed where design decisions are made, because that's where long-term value gets anchored," says BV Naidu, chairman of the Karnataka Digital Economy Mission.

Design Dominance

Karnataka accounts for roughly 10% of India's electronics production and commands about 40% of the country's electronic design activity.

ELECTRONIC MEDIA

Print - Regional

દૈનિક ભાસ્કર

ડિયા એઆઈ ઇમ્પેક્ટ સમિટ ભારતને વૈશ્વિક એઆઈ
રૂપાંતર કેન્દ્રમાં મજબૂતીથી રાખે છે : અશોક ચાંડક

ં દિલ્લી। શ્રી અશોક ચાંડક, પ્રેસિડેંટ વ સીઈઓ - ઇઆઈએસએ (ઇન્ડિયા ઇલેક્ટ્રોનિક્સ એંડ સેમીકંડક્ટર એસોસિએશન) ને કહા ઇન્ડિયા એઆઈ ઇમ્પેક્ટ સમિટ 2026, ાઈ, સેમીકંડક્ટર રણનીતિ ઔર વિશ્વસનીય વૈશ્વિક ક્નોલૉજી સાઝેદારિયોં કે મેલ મેં એક મહત્વપૂર્ણ પલ હૈ।) સે જ્યાદા રાજ્ય પ્રમુખોં, 60 મંત્રિયોં, 500 ગ્લોબલ એઆઈ ડરોં ઔર 3,00,000 સે જ્યાદા આગુંતુકોં કે સાથ, યહ મિટ ભારત કો વૈશ્વિક એઆઈ રૂપાંતર કે કેન્દ્ર મેં મજબૂતી સે રખતા હૈ - ખાસકર ગ્લોબલ

કમ્પ્યુટિંગ શક્તિ છે — અને ભારત તેનું ગ્રીડ તૈયાર કરી રહ્યું છે - અશોક ચાંડક, પ્રમુખ : IESA અને SEMI ઈન્ડિયા

માનનીય પ્રધાનમંત્રી શ્રી નરેન્દ્ર મોદીના હસ્તે આજે બોજપેલ ઈન્ડિયા AI ઈમ્પેક્ટ સમિટ એ એવા મહત્વપૂર્ણ સમયે આયોજિત થઈ છે જ્યારે ટેકનોલોજી, વેપાર અને ભૂ-રાજનીતિ એકબીજા સાથે સંગઠિત થઈ રહ્યા છે. ભારત માટે AI હવે માત્ર ડિજિટલ પરિવર્તનની કહાની નથી, પરંતુ એક ઔદ્યોગિક અને બુદ્ધાત્મક આવશ્યકતા બની ગઈ છે. આ સમિટ મજબૂત સેમિકન્ડક્ટર અને ઇલેક્ટ્રોનિક્સ ક્ષમતાઓ પર આધારિત વિશ્વસનીય અને વિસ્તૃત કરી શકાય તેવી AI ઇકોસિસ્ટમ ઊભી કરવાની ભારતની દૃઢ પ્રતિબદ્ધતા દર્શાવે છે, જે ભારત–EU FTA ચર્ચાઓ, ભારત–અમેરિકા ટેકનોલોજી અને વેપાર ભાગીદારી તેમજ ઉદયમાન સરકાર-થી-સરકાર ટેકનોલોજી કરિડોર સાથે સુસંગત છે. આ દ્વિપક્ષીય અને બહુપક્ષીય પ્રયાસોમાં એક સંદેશ સ્પષ્ટ છે— AIમાં નેતૃત્વ સુરક્ષિત સેમિકન્ડક્ટર સપ્લાય ચેઇન, ઇલેક્ટ્રોનિક સિસ્ટમ્સ, કમ્પ્યુટ ઇન્ફ્રાસ્ટ્રક્ચર અને અદ્યતન ઉત્પાદન ક્ષમતાઓ પર નિર્ભર રહેશે. વૈશ્વિક સ્તરે ટેકનોલોજી ઇકોસિસ્ટમને વૈવિધ્યપૂર્ણ અને જોખમમુક્ત બનાવવા દેશો પ્રયત્નશીલ છે, ત્યારે ડિઝાઇન, નવીનતા અને વિશ્વસનીય ઇલેક્ટ્રોનિક્સ ઉત્પાદન માટે ભારતને એક વિશ્વસનીય ભાગીદાર તરીકે જોવામાં આવી રહ્યું છે.

देशदावा LIVE

AI at Scale: India Builds the Full Stack-from Silicon to Sovereign Compute

Bhubaneswar : The India AI Impact Summit 2026 marks a defining moment in the convergence of AI, semiconductor strategy, and trusted global technology partnerships. With participation from over 20 Heads of State, 60 Ministers, 500 global AI leaders, and more than 300,000 attendees, the Summit firmly positions India at the center of the global AI transformation especially for the Global South. The release of India's AI Governance Guidelines, the expansion of the India AI Fellowship to 13,500 scholars, and the strengthening of the India AI Safety Institute demonstrate a comprehensive approach policy, talent, and standards moving in alignment. On the infrastructure front, with 38,000 GPUs already operational and over 50,000 more being deployed in the coming months, India is rapidly scaling sovereign compute capacity. The AI momentum is inseparable from semiconductor momentum. India is building the full innovation stack-design, compute, manufacturing, and deployment-anchored in trust, scale, and sustainability said Mr Ashok Chandak, President and CEO - IESA (India Electronics & Semiconductor association).

दैनिक सवेरा टाइम्स

एआई मोमेंटम को सेमीकंडक्टर मोमेंटम से अलग नहीं किया जा सकता है - अशोक चांडक, प्रेसिडेंट व सीईओ - आईईएसए

देश प्रदेश टाइम्स

Deccan Vision

English Daily

Compute is Power—India Building Grid

IESA

Taking India to FSDH Leadership

India signs Pax Silica declaration at AI Impact Summit

Chandak calls it 'Silicon Shield' for digital sovereignty

Best Performing Posts

At a pivotal moment for India's semiconductor and electronics journey, #IESA and Analog Devices (ADI), hosted a high-level leadership roundtable bringing together senior leaders from #industry, #academia, #startups, and the broader #semiconductor ecosystem.

The interaction was marked by the presence of Mr. Vincent Roche, President & CEO, Analog Devices, Mr. Ray Stata, Analog Devices, Mr. Vivek Tyagi, MD Analog Device India and Several #ESDM leaders in India. The participation of ADI's founder underscored a powerful message : this is not short-term interest, but a long-term commitment to India's semiconductor future.

The dialogue reflected growing maturity of India's ecosystem, spanning policy, design, manufacturing, talent, capital, innovation, and aligned strongly with #national initiatives like India Semiconductor Mission, #Electronics #PLI schemes, and collaborative platforms led by #IESA SEMI.
Discussions focused on global industry shifts, emerging opportunities in #analog , #innovation, and collective actions required to build a globally competitive & trusted #semiconductor value chain in #India.

As global supply chains realign, India's progress is driven by stakeholders collaborations; the foundation on which sustainable semiconductor ecosystems are built. What stood out was the breadth & diversity of viewpoints at the table. The conversation captured how India's semiconductor ecosystem is no longer limited to aspiration but is rapidly evolving into integrated value chain, with strengths in design, growing manufacturing ambitions, a strong startup pipeline, and a robust talent base.

"Great semiconductor ecosystems are not built in quarters, but over decades through trust, talent, and enduring partnerships."
Inspired by the legacy of Analog Devices

The #roundtable enabled an in-depth dialogue between Mr. Roche and a distinguished group ecosystem leaders, including Akshay Aggarwal (MediaTek and EC, IESA), Ms. Arundhati Sanath (Lam Research), Prof. Debabrata D. (Indian Institute of Technology, Bombay), Mr. Ganapathy Subramaniam (Yali Capital), Mr. Hitesh Garg (NXP Semiconductors), Ms. Malini Narayanamoorthi (Renesas Electronics), Ms. Radhika Viswanathan (Applied Materials), Mr. Ruchir Dixit (Siemens EDA (Siemens Digital Industries Software) and Chairperson, IESA), Mr. Srikanth Settikere (Microchip Technology Inc.), Mr. Vinay Shenoy (Infineon Technologies) and #ADI Management team.

IESA, SEMI and it's members remains committed to enabling leadership driven engagements that accelerates its semiconductor journey.

Ministry of Electronics and Information Technology Ashwini Vaishnaw S Krishnan Amitesh Kumar Sinha

#SEMI Ajit Manocha
Navin Bishnoi Rajeev Khushu Sanjeev Keskar Veerappan VV ashok chandak Akshay Aggarwal Dr. Hemang Shah Pradeep Vajram Raghu Panicker Sundeep Gupta #VivekTyagi #VedPrakashMall

Impressions: 19107

Engagements: 2670

Impressions: 4464

Clicks: 2788

Day by Day #Highlights of the visit:

• Day 1 - Indian Institute of Science (IISc) Bengaluru
Participants explored #AdvancedResearch labs, interdisciplinary innovation culture, and strong academia-industry linkages. The visit demonstrated how research-driven ecosystems can translate fundamental science into #ScalableTechnologies. Thanks to Kalpana Subbaramappa (CenSe) for giving such exposure to #Academia professors.

• Day 2 - GlobalFoundries & IESA Office
Faculty members gained exposure to #SemiconductorManufacturing processes, industry-focused R&D, and #FabricationWorkflows at GlobalFoundries. A subsequent visit to the IESA office provided deeper insights into national workforce development priorities and #EcosystemBuilding efforts. Thanks to Dibyendu Ghosh for giving such exposure to Academia professors.

• Day 3 - Tessolve Semiconductor
The delegation experienced #VLSIDesign, #Testing, #ReliabilityValidation, and #ProductEngineering environments. Thanks to Sudhakar Rajamanickam for giving such exposure to #Academia professors.
We extend our sincere appreciation to Dr. Veerappan VV, IESA - Advisor to the Board, Co-Founder -Tessolve, for his inspiring address on the importance of transforming real-world industry knowledge to the classroom.

#KeyOutcomes of the Initiative
✔ A strong #PANIndia academic presence with educators from multiple regions meaningfully engaging with the semiconductor ecosystem.
✔ Bridging the #AcademiaIndustry gap through direct exposure to #Design, #Manufacturing, and #Testing environments.
✔ Participants shared structured #LearningOutcomes, highly positive feedback, and a renewed understanding of industry expectations, tools, and workflows.
✔ Reinforced the role of continuous academia-industry research collaboration in shaping an #IndustryReady #EngineeringWorkforce.

IESA thanks all participating #Institutions and #FacultyMembers, IISc Bengaluru, GlobalFoundries & Tessolve Semiconductor for their openness & knowledge sharing. #InstitutionalManagements for enabling faculty participation.

IESA remains committed to building future-ready semiconductor talent ecosystem through #SustainedCollaboration.

Ministry of Electronics and Information Technology India Semiconductor Mission Viswanathan K.G.
Navin Bishnoi Rajeev Khushu Sanjeev Keskar ashok chandak Akshay Aggarwal Dr. Hemang Shah 💡 Pradeep Vajram Raghu Panicker Sundeep Gupta #VivekTyagi #VedPrakashMall

IESA Industry-Academia 3 Day Visit • 13 pages

Clicks: 2768

Engagement: 1867

Thank You!